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To Examiner J. Kennedy

OFFICIAL

Fax no. 703-872-9306

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From Mitchell W. Shapiro

Phone no.

Date January 28, 2004

Time

File no. U.S. Appl. No. 09/989,061

Pages including cover 3

Message

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XA-9574

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Hideki YASUOKA et al.

Appln. No.: 09/989,061

Group Art Unit: 2812

Filed: November 21, 2001

Examiner: J. Kennedy

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND METHOD  
OF MANUFACTURING THE SAME

Allowed: December 19, 2003

Confirmation No.: 1044

\* \* \*

REQUEST FOR REJOINDER AND  
ALLOWANCE OF NON-ELECTED DEPENDENT CLAIMS

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Attn: Mail Stop Issue Fee

Sir:

Referring to the Examiner's amendment accompanying the Notice of Allowability, Applicants note that non-elected Claims 8, 22, and 28-39 were cancelled by the Examiner. However, Claims 8 and 22 should have been rejoined and allowed in view of their dependence from allowed base Claims 1 and 13, respectively.

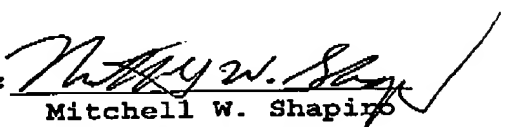
Applicants respectfully request that the Office  
furnish a Supplemental Notice of Allowability to confirm  
the rejoinder and allowance of Claims 8 and 22.

Respectfully submitted,

MWS:lmb

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By:

  
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January 28, 2004

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that this paper is being facsimile  
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date shown below.

  
Mitchell W. Shapiro

January 28, 2004  
Date